

IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): DODD, SIMON

Confirmation No.: 9553

Application No.: 10/055,161

Examiner: LATTIN, C.W.

Filing Date: Oct 26, 2001

Group Art Unit: 2812

Title: DEVICES AND METHODS FOR INTEGRATED CIRCUIT MANUFACTURING

COMMISSIONER FOR PATENTS
Washington, D.C. 20231

TRANSMITTAL LETTER FOR RESPONSE/AMENDMENT

Sir:

Transmitted herewith is/are the following in the above-identified application:

- (X) Response/Amendment () Petition to extend time to respond
() New fee as calculated below () Supplemental Declaration
() No additional fee (Address envelope to "Box Non-Fee Amendments")
(X) Other: IDS; PTO-1449 (fee \$)

CLAIMS AS AMENDED BY OTHER THAN A SMALL ENTITY						
(1) FOR	(2) CLAIMS REMAINING AFTER AMENDMENT	(3) NUMBER EXTRA	(4) HIGHEST NUMBER PREVIOUSLY PAID FOR	(5) PRESENT EXTRA	(6) RATE	(7) ADDITIONAL FEES
TOTAL CLAIMS	20	MINUS	22	= 0	X \$18	\$ 0
INDEP. CLAIMS	5	MINUS	7	= 0	X \$84	\$ 0
[] FIRST PRESENTATION OF A MULTIPLE DEPENDENT CLAIM					+ \$280	\$ 0
EXTENSION FEE	1ST MONTH \$110.00	2ND MONTH \$410.00	3RD MONTH \$930.00	4TH MONTH \$1450.00		\$ 0
OTHER FEES						\$
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT						\$ 0

Charge \$ 0 to Deposit Account 08-2025. At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account 08-2025 pursuant to 37 CFR 1.25. Additionally please charge any fees to Deposit Account 08-2025 under 37 CFR 1.16 through 1.21 inclusive, and any other sections in Title 37 of the Code of Federal Regulations that may regulate fees. A duplicate copy of this sheet is enclosed.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231.

Date of Deposit: Feb 24, 2003

Typed Name: Collette Angle-Olson

Signature: Collette Angle-Olson

Respectfully submitted,

DODD, SIMON

By

Peter J. Reitan

Attorney/Agent for Applicant(s)

Reg. No. 48,603

Date: Feb 24, 2003



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

DODD et al.

Serial No.: 10/055,161

Filed: 10/26/2001

For: DEVICES AND METHODS FOR
INTEGRATED CIRCUIT
MANUFACTURING

Group Art Unit: 2812

Examiner: LATTIN, C.W.

#1013
And
McMillan
3/18/03

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RESPONSE TO OFFICE ACTION

Commissioner for Patents
Washington, D.C. 20231

Sir:

This is in response to an office action mailed November 29, 2002
in the above-referenced application.

IN THE CLAIMS

Please amend claims 15-16, 19 and , 23-25 as follows:

15. (Amended) The process of claim 14, wherein the step of forming at
least an insulating layer comprises growing a gate oxide layer.

B1

16. (Amended) The process of claim 15, wherein the step of forming at
least an insulating layer further comprises depositing a gate electrode layer.